IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): H.S. Nagesh et al. Case:

10-26-12-10-40

Serial No.:

To Be Assigned

Filing Date: Title:

February 24, 2004 Load Balancing Method and Apparatus for

Ethernet over SONET and Other Types of Networks

Group:

To Be Assigned To Be Assigned

Examiner:

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed document is enclosed.

- 1. S. Keshav et al., "Issues and Trends in Router Design," IEEE Communication Magazine, 15 pages, 1998.
- 2: C-S. Chang et al., "Load Balanced Birkhoff-von Neumann Switches, Part I: One-stage Buffering," Computer Communications, pp. 1-25, 2001.
- 3. L.G. Valiant, "A Scheme for Fast Parallel Communication," SIAM Journal of Computing, Volume 11, No. 2, pp. 350-361, 1982.
- 4. ITU-T Recommendation G.7042/Y.1305 Corrigendum 1, "Link Capacity Adjustment Scheme (LCAS) for Virtual Concatenated Signals," 16 pages, 2002.
- 5. ITU-T Recommendation G.7042/Y.1305, "Link Capacity Adjustment Scheme (LCAS) for Virtual Concatenated Signals," 24 pages, 2001.

6. ITU-T Recommendation G.707/Y.1322, "Network Node Interface for the Synchronous Digital Hierarchy," 184 pages, 2000.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

Joseph B. Ryan

Date: February 24, 2004

Reg. No. 37,922

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FORM PTO-1449 (MODIFIED)

LIST OF PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT Applicant(s): H.S. Nagesh et al.

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U.S. PATENT DOCUMENTS FILING DATE **EXAMINER** CLASS/SUBCLASS IF APPROPRIATE NAME INITIAL DOCUMENT NO. DATE FOREIGN PATENT DOCUMENTS TRANSLATION **EXAMINER** CLASS/SUBCLASS COUNTRY YES DOCUMENT NO. DATE INITIAL OTHER DOCUMENTS **EXAMINER** AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC. INITIAL REF NO. 1. S. Keshav et al., "Issues and Trends in Router Design," IEEE Communication Magazine, 15 pages, 1998. 2. C-S. Chang et al., "Load Balanced Birkhoff-von Neumann Switches, Part I: One-stage Buffering," Computer Communications, pp. 1-25, 2001. 3. L.G. Valiant, "A Scheme for Fast Parallel Communication," SIAM Journal of Computing, Volume 11, No. 2, pp. 350-361, 1982. 4. ITU-T Recommendation G.7042/Y.1305 - Corrigendum 1, "Link Capacity Adjustment Scheme (LCAS) for Virtual Concatenated Signals," 16 pages, 2002. 5. ITU-T Recommendation G.7042/Y.1305, "Link Capacity Adjustment Scheme (LCAS) for Virtual Concatenated Signals," 24 pages, 2001. ITU-T Recommendation G.707/Y.1322, "Network Node Interface for the Synchronous Digital Hierarchy," 184 pages, 2000. Examiner **Date Considered**

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.